



Attorney Docket No. 5308-286

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Sumakeris et al.

Serial No.: 10/686,795

Filed: October 16, 2003

For: METHODS OF FORMING POWER SEMICONDUCTOR DEVICES USING BOULE-GROWN SILICON CARBIDE DRIFT LAYERS AND POWER SEMICONDUCTOR DEVICES FORMED THEREBY

Group Art Unit: 2811

Examiner: To Be Assigned

Confirmation No.: 7928

Date: March 31, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP. No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

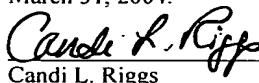
Respectfully submitted,


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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 31, 2004.


Candi L. Riggs

